



VENUS GROUP OF EDUCATIONAL AND RESEARCH INSTITUTES, BAHANAGA, BALASORE

TIME TABLE FOR SUMMER-2026 CLASSES, W.E.F.Date- 22/12/2025

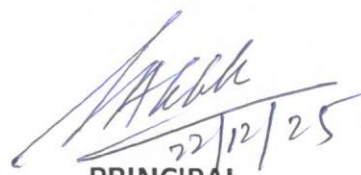
BRANCH: ELECTRONICS & T.C. ENGG.

SEMESTER: 4TH

CLASS ROOM NO.: 07

TIME DAYS	10:00 - 10:55	10:55 - 11:50	11:50 - 12:45	12:45 - 1:40	L U N C H B R E A K	2:20 - 3:15	3:15 - 4:10
MON	FUNDAMENTAL OF POWER ELECTRONICS	ANALOG AND DIGITAL COMMUNICATION	ANALOG ELECTRONICS	EMBEDED SYSTEMS		POWER ELECTRONICS LAB	
TUE	ANALOG ELECTRONICS	FUNDAMENTAL OF POWER ELECTRONICS	ANALOG AND DIGITAL COMMUNICATION	MICROPROCESSOR & MICROCONTROLLER		ANALOG ELECTRONICS LAB	
WED	ANALOG ELECTRONICS	ANALOG AND DIGITAL COMMUNICATION	FUNDAMENTAL OF POWER ELECTRONICS	EMBEDED SYSTEMS		MICROPROCESSOR & MICROCONTROLLER LAB	
THU	ANALOG AND DIGITAL COMMUNICATION	MICROPROCESSOR & MICROCONTROLLER	ANALOG ELECTRONICS	FUNDAMENTAL OF POWER ELECTRONICS		ANALOG AND DIGITAL COMMUNICATION LAB	
FRI	MICROPROCESSOR & MICROCONTROLLER	EMBEDED SYSTEMS	POWER ELECTRONICS LAB			MINOR PROJECT	
SAT	EMBEDED SYSTEMS	MICROPROCESSOR & MICROCONTROLLER	ANALOG ELECTRONICS LAB			ANALOG AND DIGITAL COMMUNICATION LAB	

SUBJECT ASSIGNMENTS (THEORY)	
FUNDAMENTAL OF POWER ELECTRONICS	ER. ANIL KU . BARIK
ANALOG AND DIGITAL COMMUNICATION	ER.SULOCHANA DAS
MICROPROCESSOR & MICROCONTROLLER	L1
EMBEDED SYSTEMS	L2


 22/12/25
PRINCIPAL
 VGERI, BAHANAGA
 Principal,
 Venus Group of Educational
 & Research Institute
 Bahanaga, Balasore

SUBJECT ASSIGNMENTS (PRACTICAL)	
FUNDAMENTAL OF POWER ELECTRONICS LAB	ER. ANIL KU. BARIK
ANALOG AND DIGITAL COMMUNICATION	ER.SULOCHANA DAS
MICROPROCESSOR AND MICROCONTROLLER LAB	L1
MINOR PROJECT	ER.SULOCHANA DAS
ANALOG ELECTRONICS LAB	L2